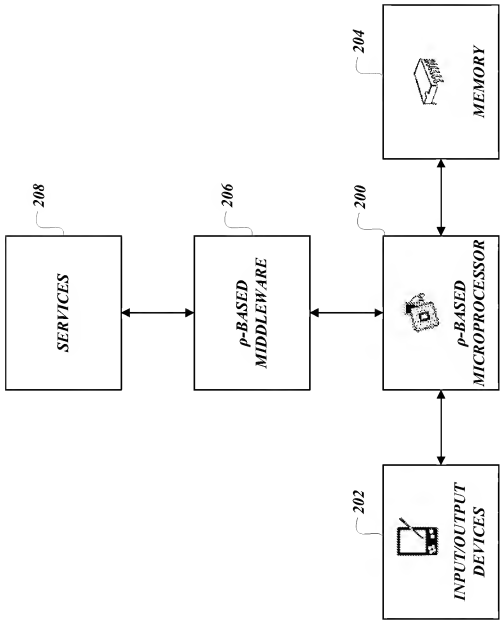
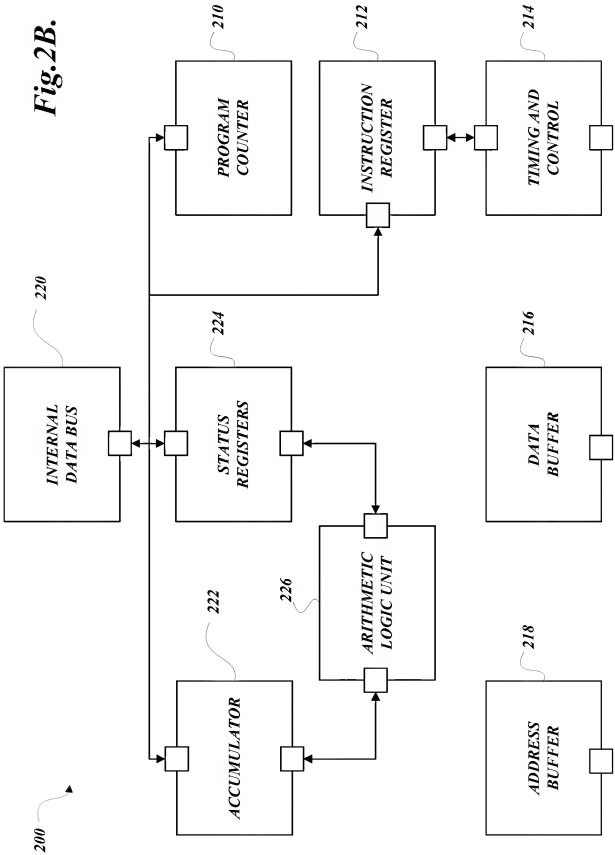

208
SERVICES
206
ρ-BASED
MIDDLEWARE
200
ρ-BASED
MICROPROCESSOR
204
MEMORY
202
INPUT/OUTPUT
DEVICES

Fig.2A.

Fig.2B.


200
INTERNAL DATA BUS
220
ACCUMULATOR
222
ARITHMETIC LOGIC UNIT
226
STATUS REGISTERS
224
PROGRAM COUNTER
210
INSTRUCTION REGISTER
212
TIMING AND CONTROL
214
DATA BUFFER
216
ADDRESS BUFFER
218

500
MACHINE CALCULUS
P ::= 0 502
X[Y] 504
X(Z) . P 506
LIFT X . P 508
P|P 510
⟩X⟨ 512
X ::= ⟨P⟩,#(P) ≤ K 514
Y ::= ⟨P⟩,P:T 516
Z ::= ⟨P⟩,P:U 518

Fig.5A.


522
522C
VELOCITY
522D
522F
MILES
522E
=
522H
HOUR
522G
522A
522B
520C
ACCELERATION
520D
520F
MILES
520E
=
520H
HOUR
520G
520A
520I
HOUR
520J
520B
520

Fig.5B.